

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR REDUCED ELECTRICAL FUSING TIME	
Application Number :		
Date :		
First Named Applicant:	Darren L. Anand	
Confirmation Number:		
Attorney Docket Number:	BUR920020093US1	
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Documents being submitted:

us-request

us-assignment

us-fee-sheet

us-ids

us-declaration

us-declaration

us-declaration

application-body

Files

BUR920020093US1-usrequ.xml

us-request.dtd

us-request.xsl

BUR920020093US1-usassn.xml

us-assignment.xsl

us-assignment.dtd

BUR920020093assign1.tif

BUR920020093assign2.tif

BUR920020093US1-usfees.xml

us-fee-sheet.xsl

us-fee-sheet.dtd

BUR920020093US1-usidst.xml

us-ids.dtd

us-ids.xsl

BUR920020093dec1.tif

BUR920020093dec2.tif

BUR920020093dec3.tif

BUR920020093spec-trans.xml

us-application-body.xsl

application-body.dtd

wipo.ent

mathml2.dtd

mathml2-qname-1.mod

isoamsa.ent

isoamsb.ent

isoamsc.ent

isoamsn.ent

isoamso.ent

isoamsr.ent

isogr3.ent

isomfrk.ent

isomopf.ent

isomscr.ent

isotech.ent

isobox.ent

isocyr1.ent

isocyr2.ent

isodia.ent

isolat1.ent

isolat2.ent

isonum.ent

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mmlextra.ent

mmlalias.ent

soextblk.dtd

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Comments